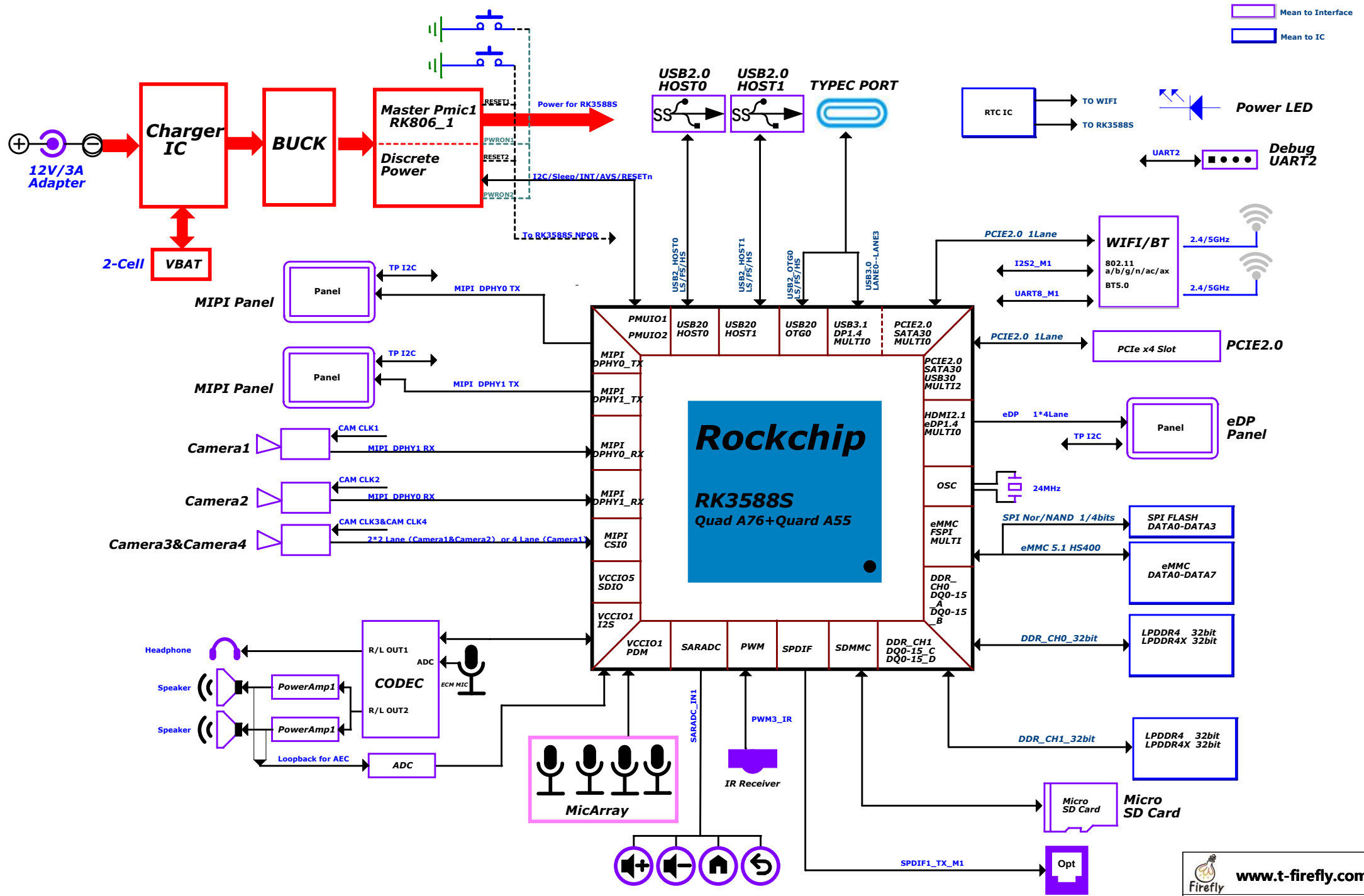
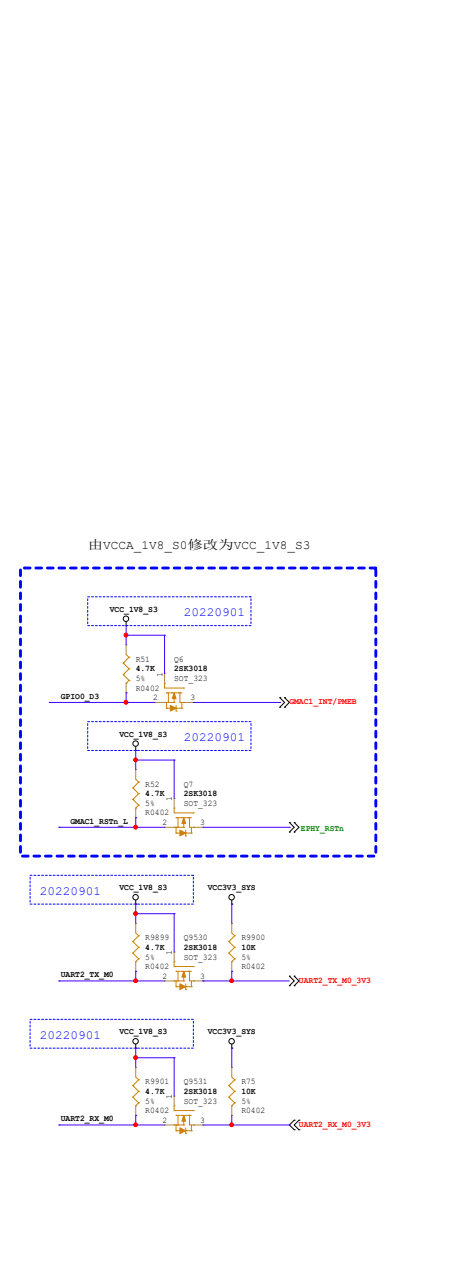
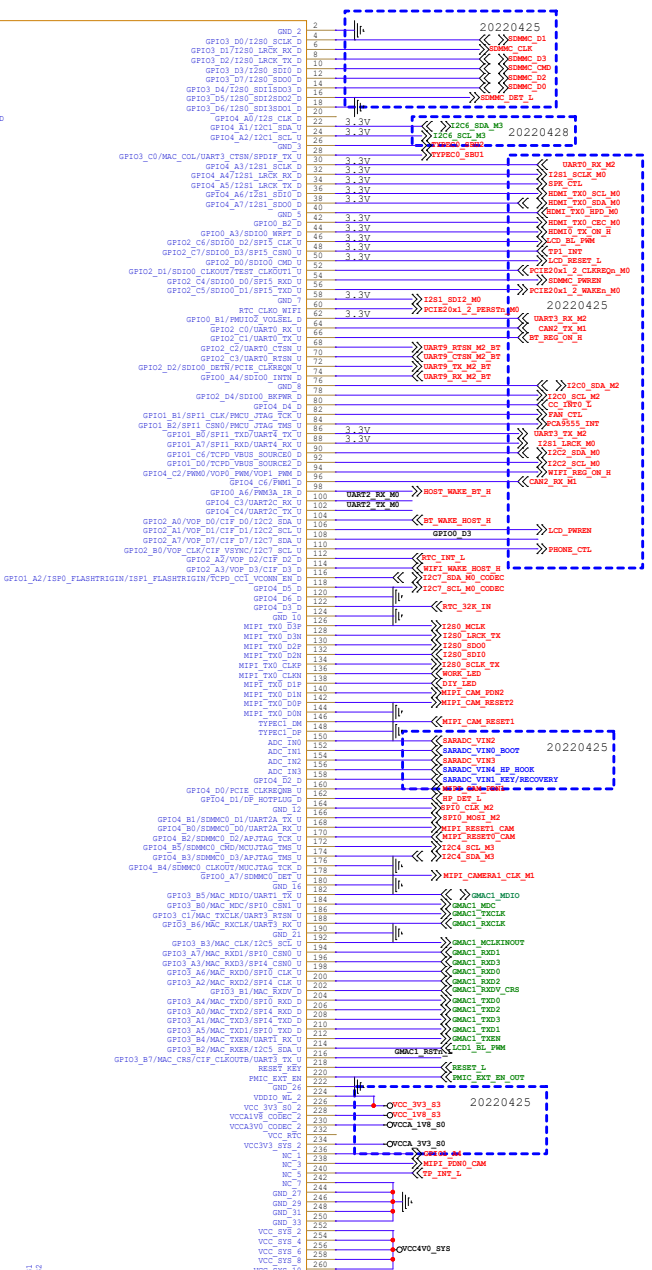
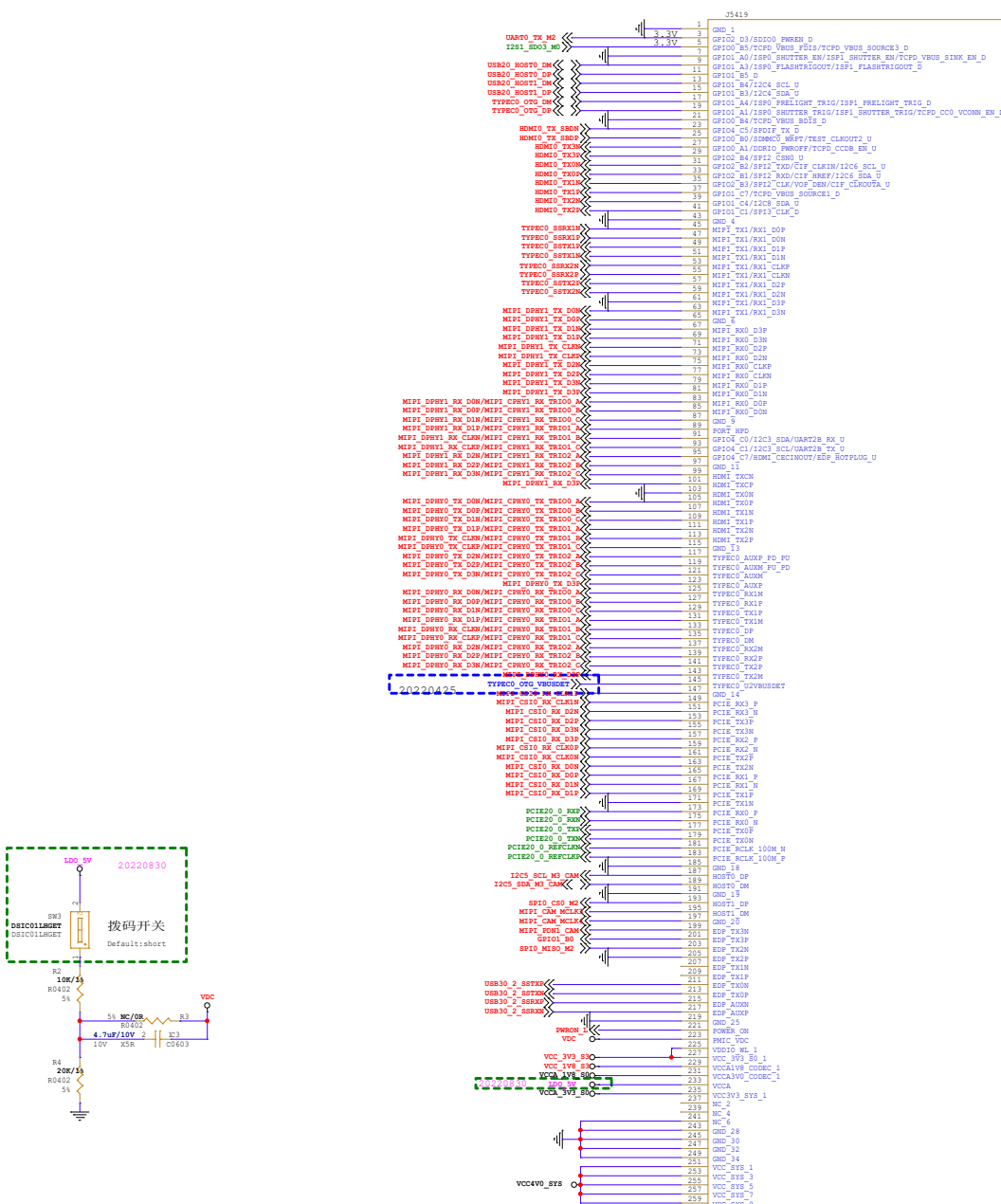
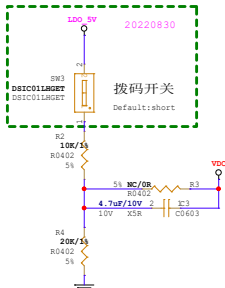
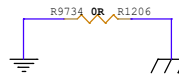
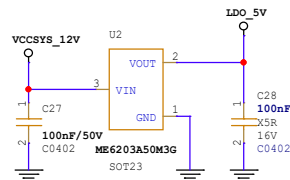
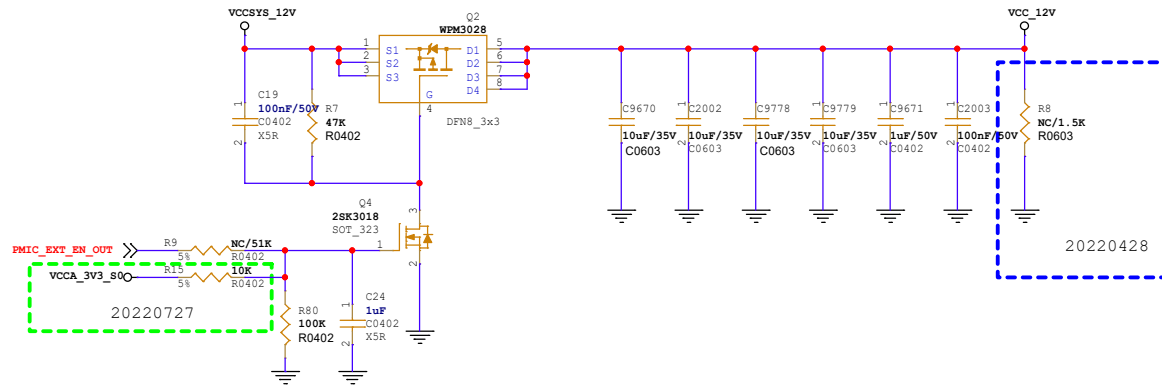
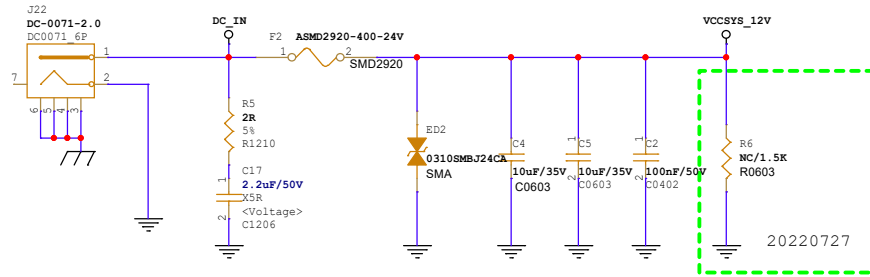


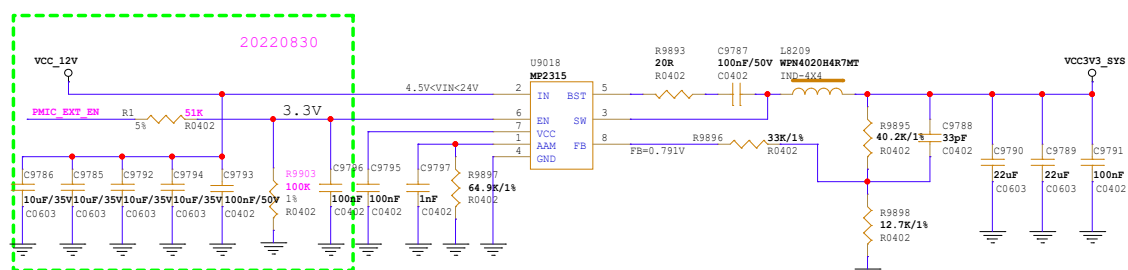
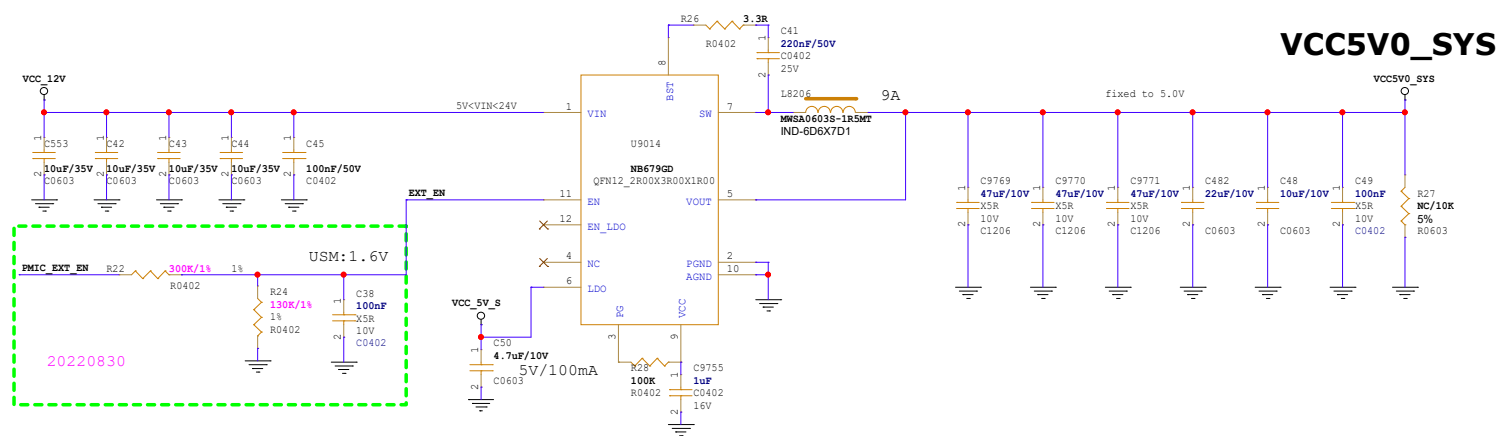
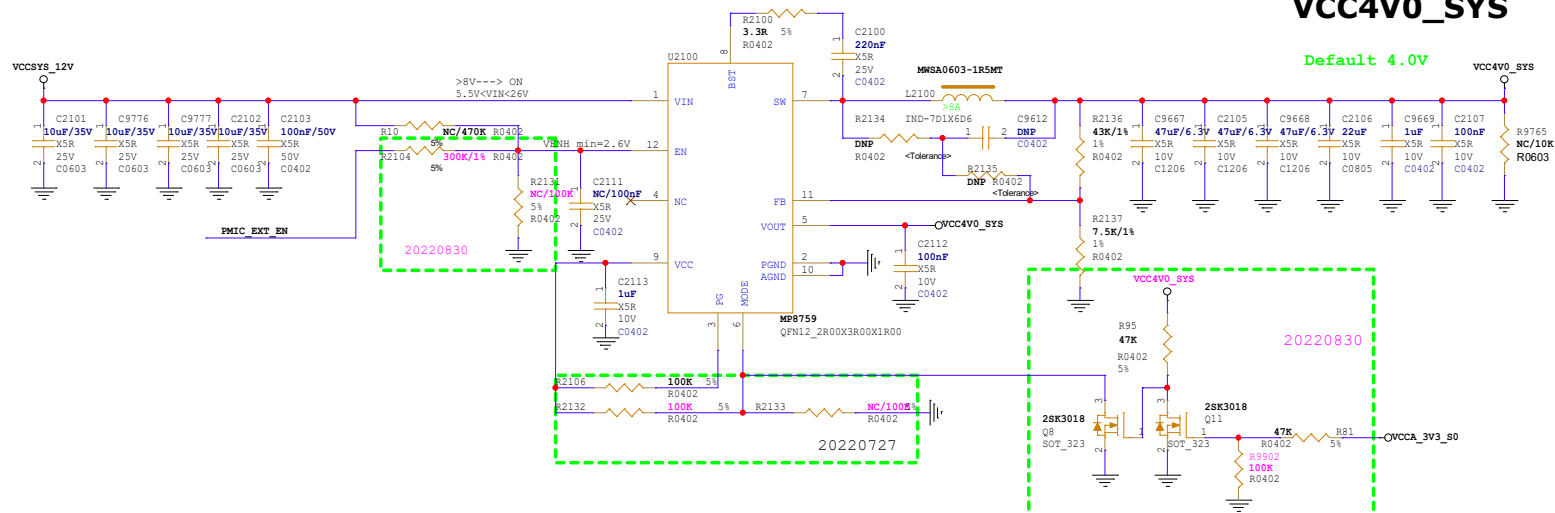
RK3588S EVB Block Diagram



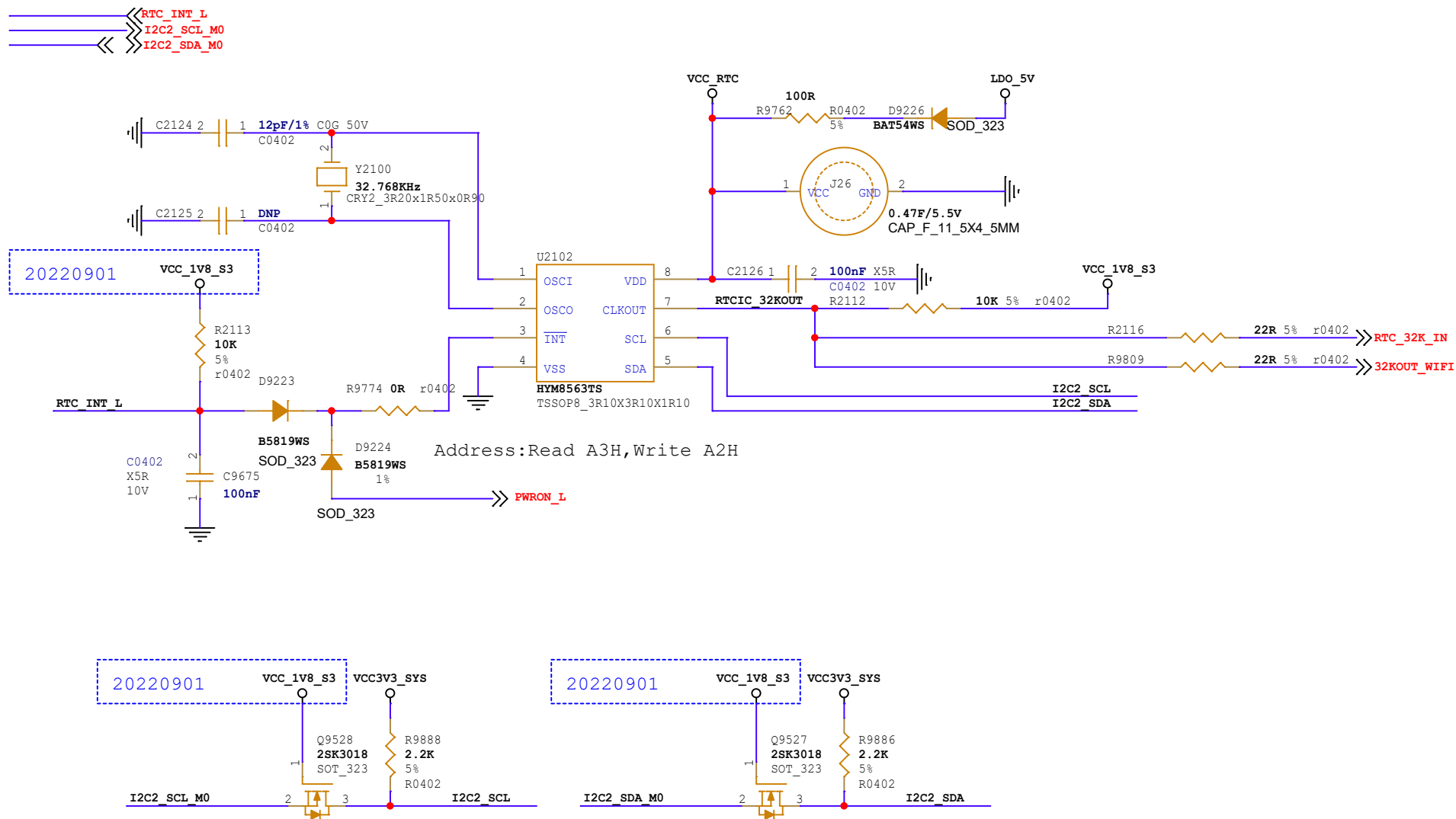


DC_IN

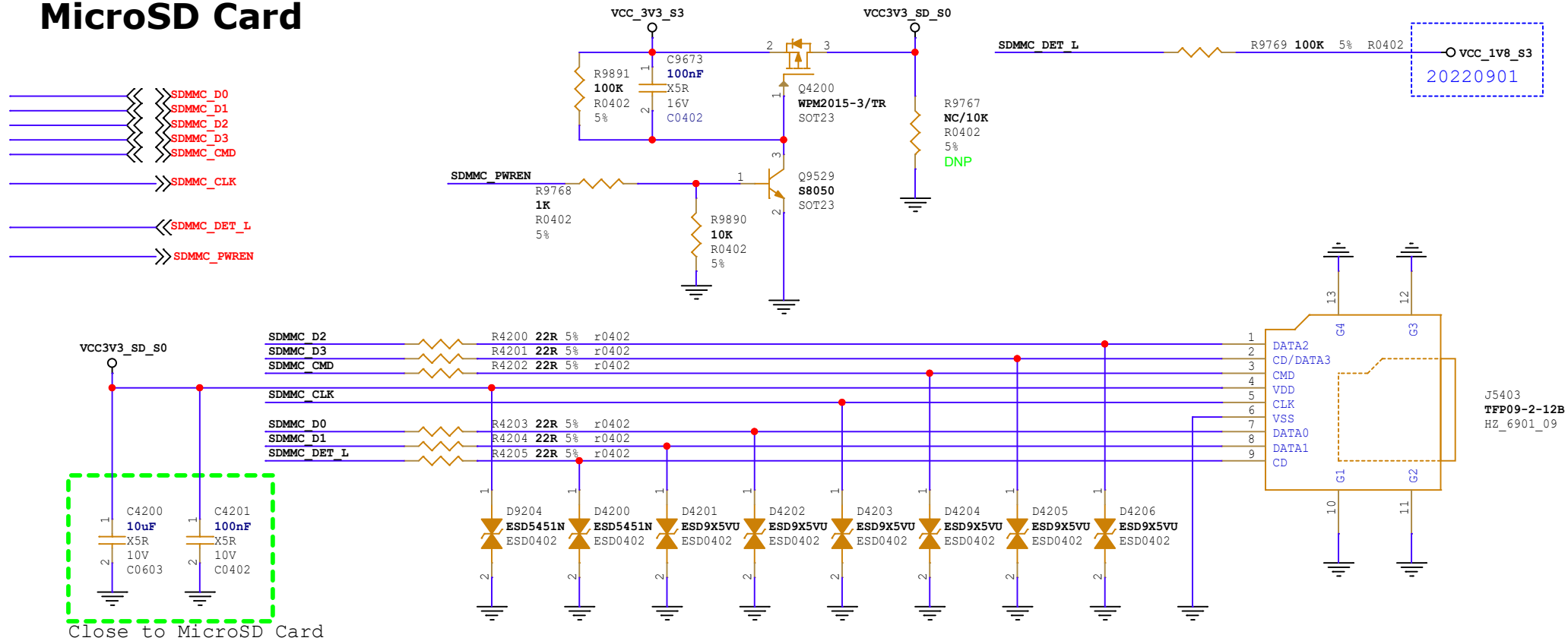




RTC IC



MicroSD Card



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Project: Schematic Name

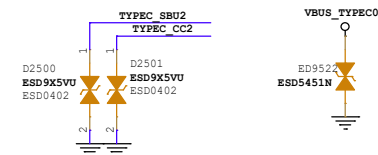
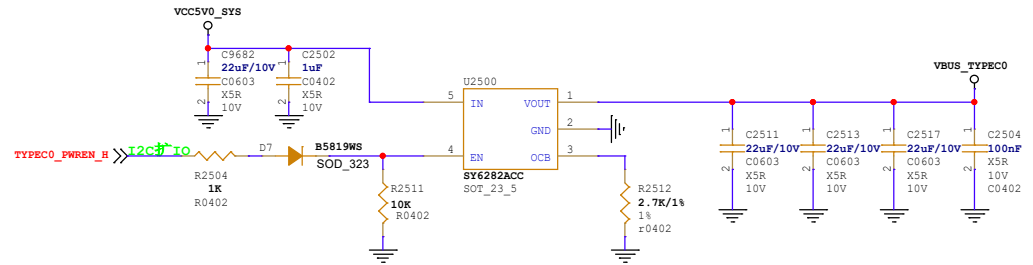
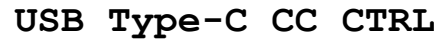
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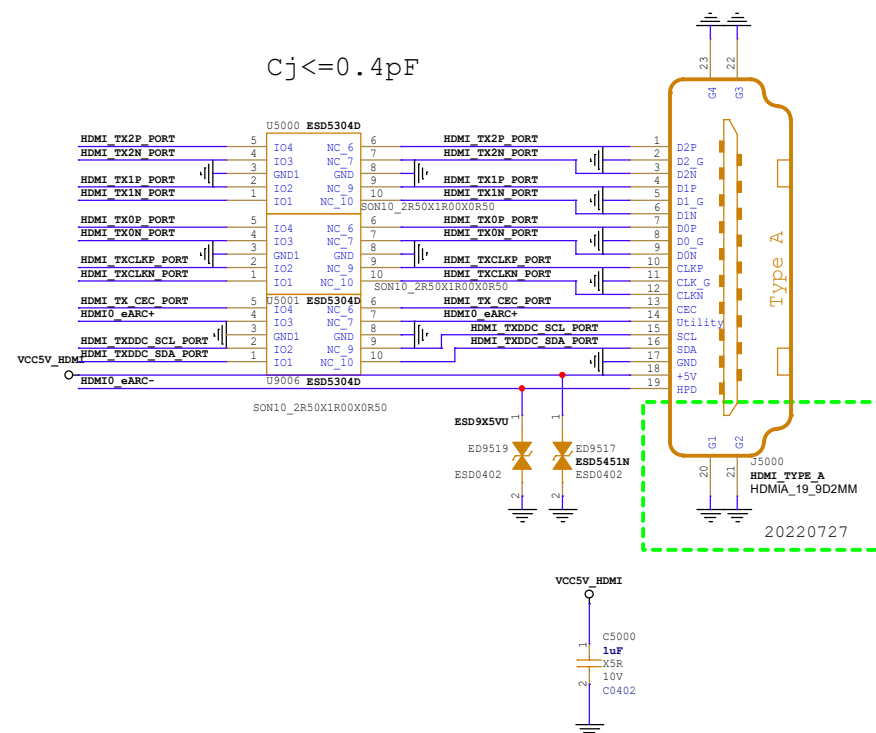
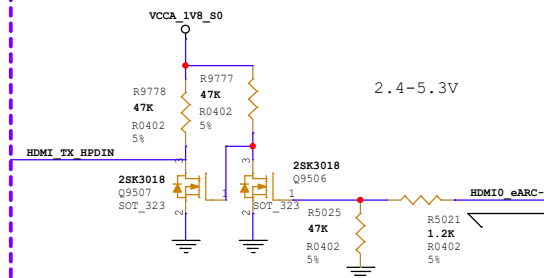
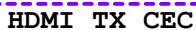
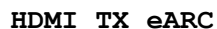
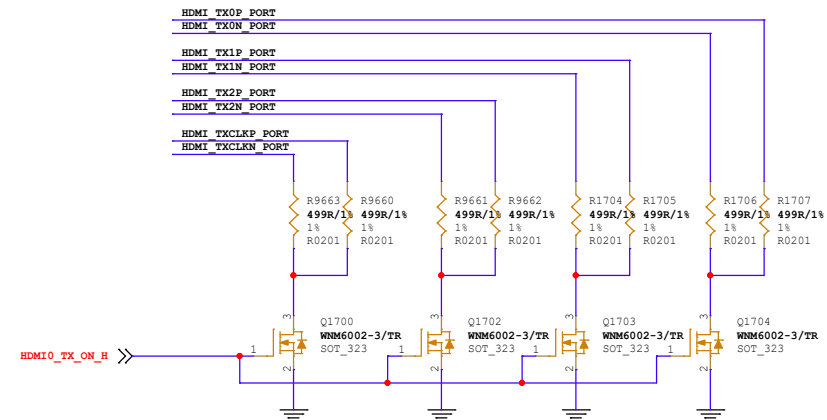
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Create Date: Tuesday, December 28, 2022 Page Num:6

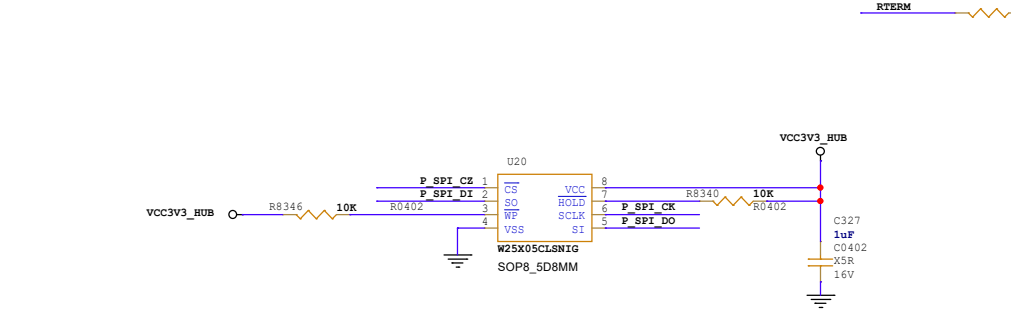
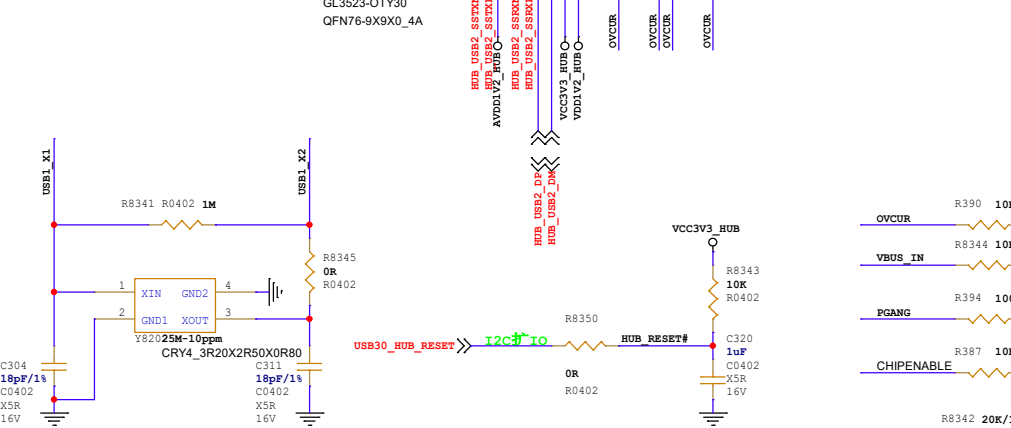
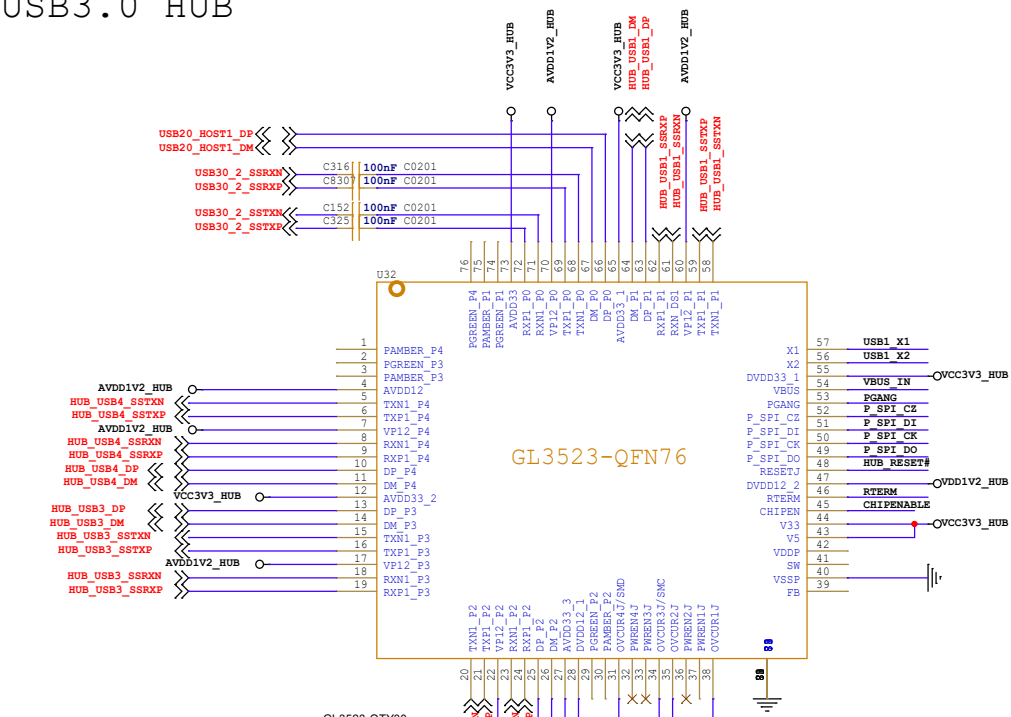
Modify Date:	Thursday, September 01, 2022	Page Total:31
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USB Detection

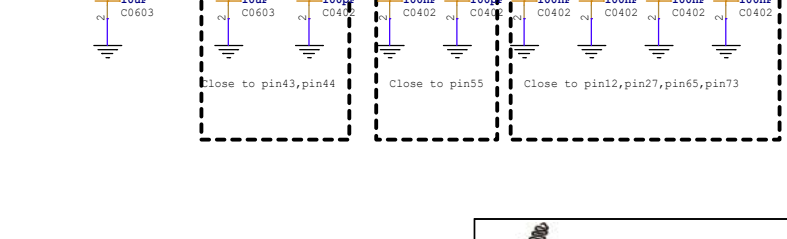
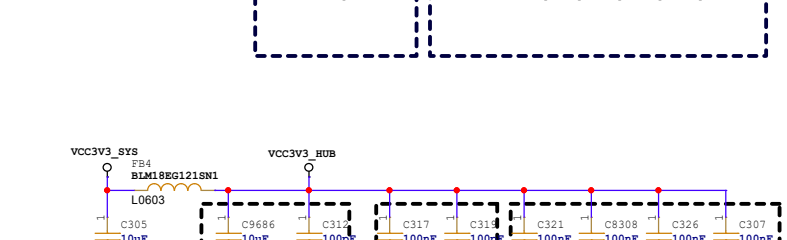
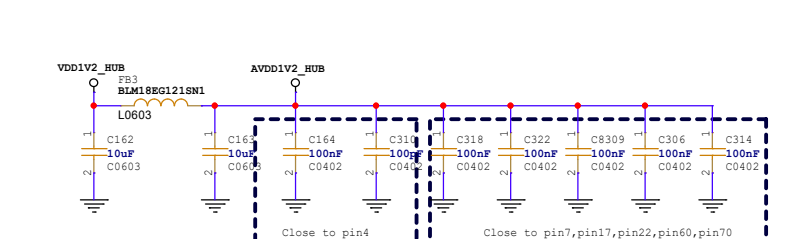
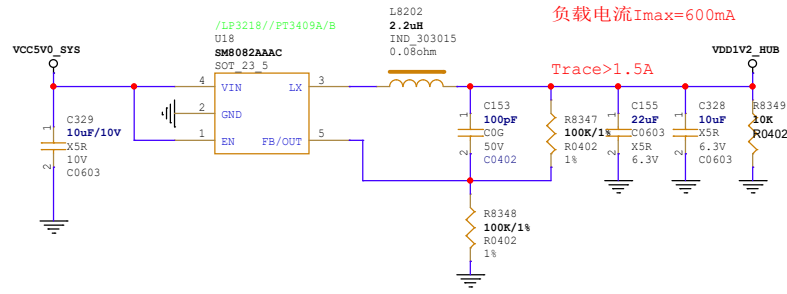




USB3.0 HUB



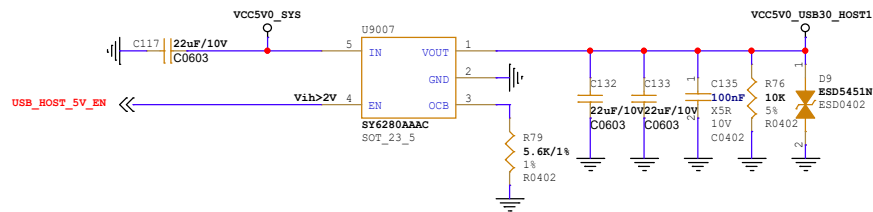
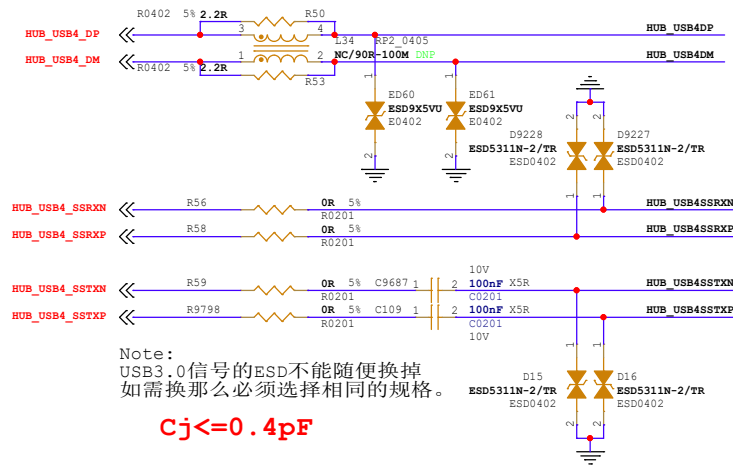
HUB_POWER



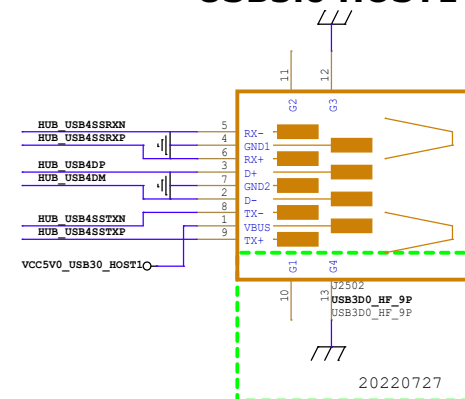


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File: MB-RK3588S-JD4	REV: V1.0
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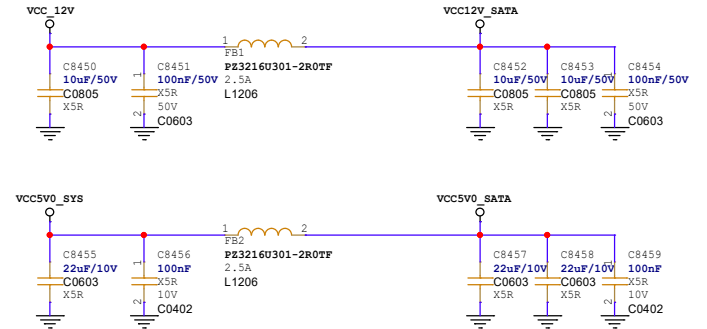


USB3.0 HOST1

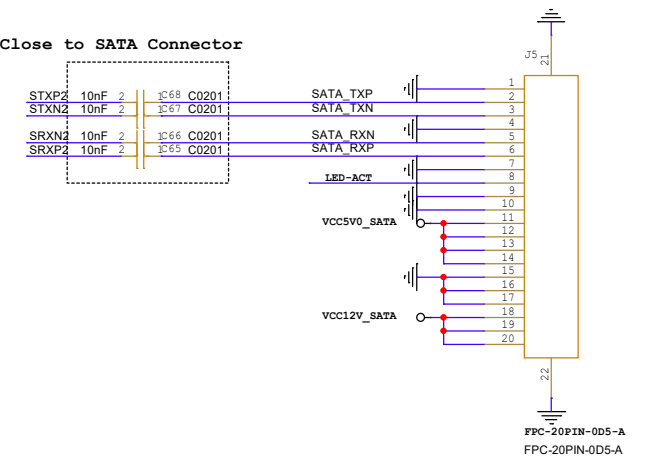


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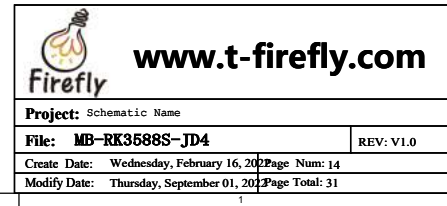
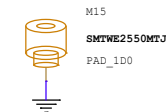
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Create Date: Wednesday, February 16, 2022	Page Num: 10
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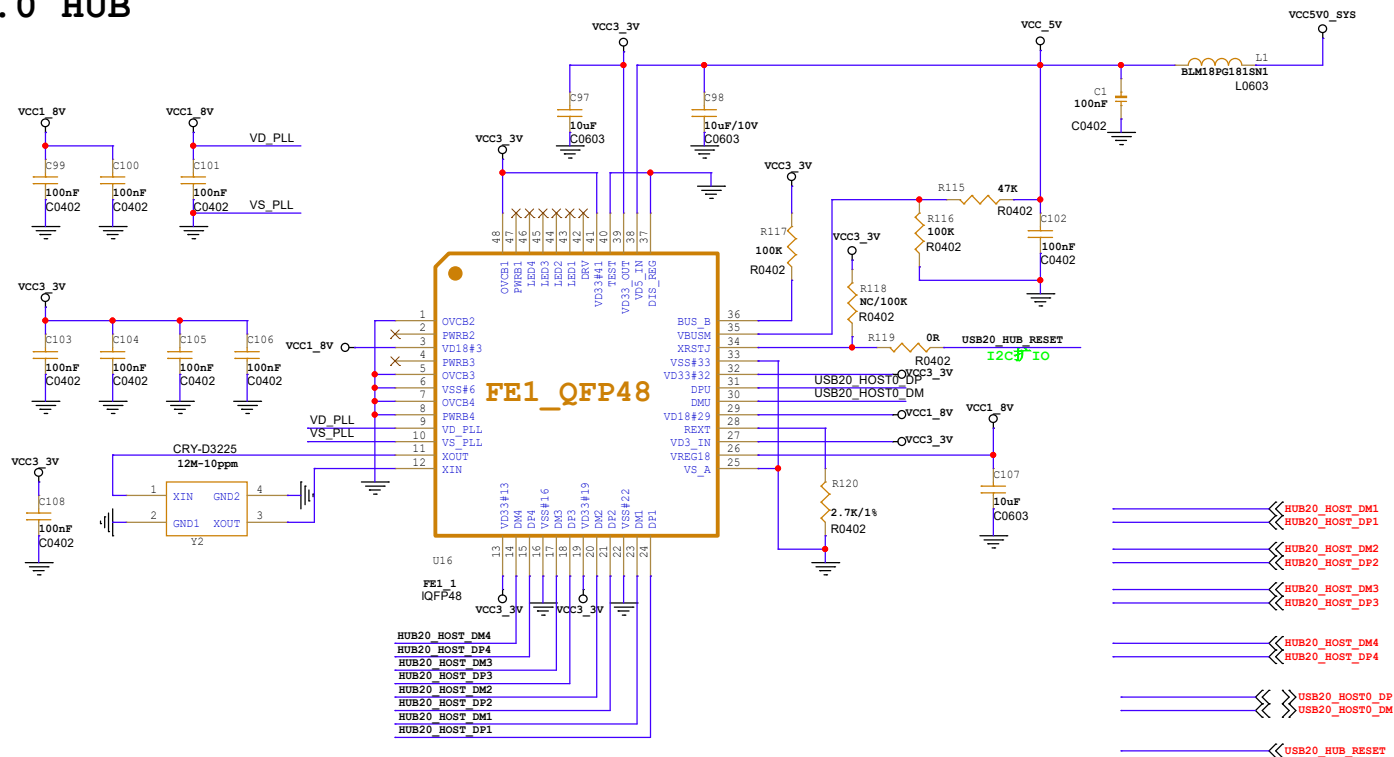
Close to SATA Connector

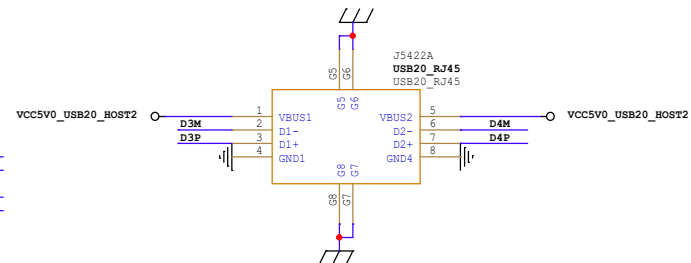
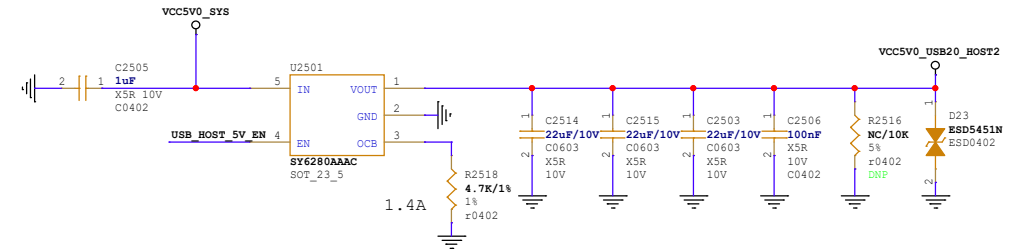
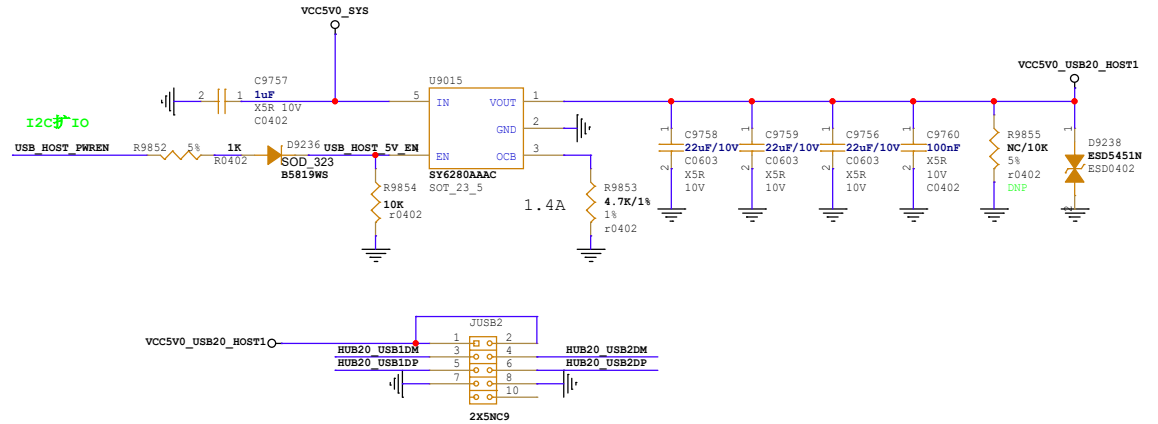
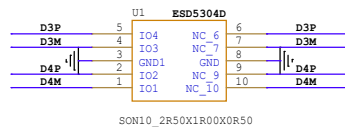
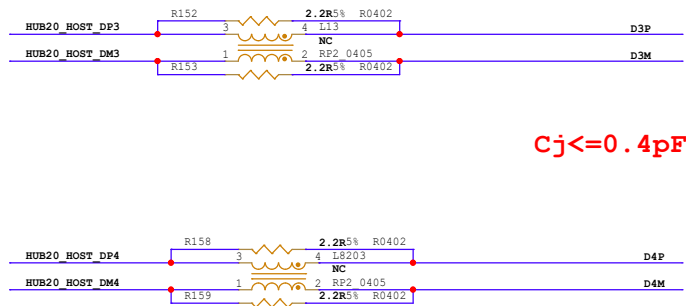
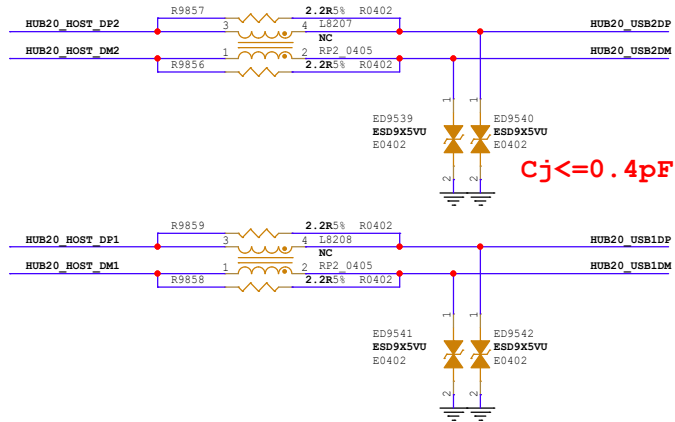
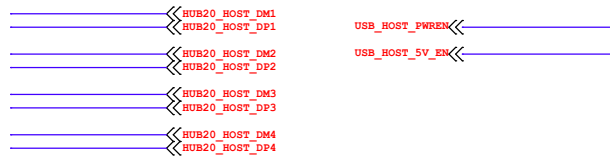


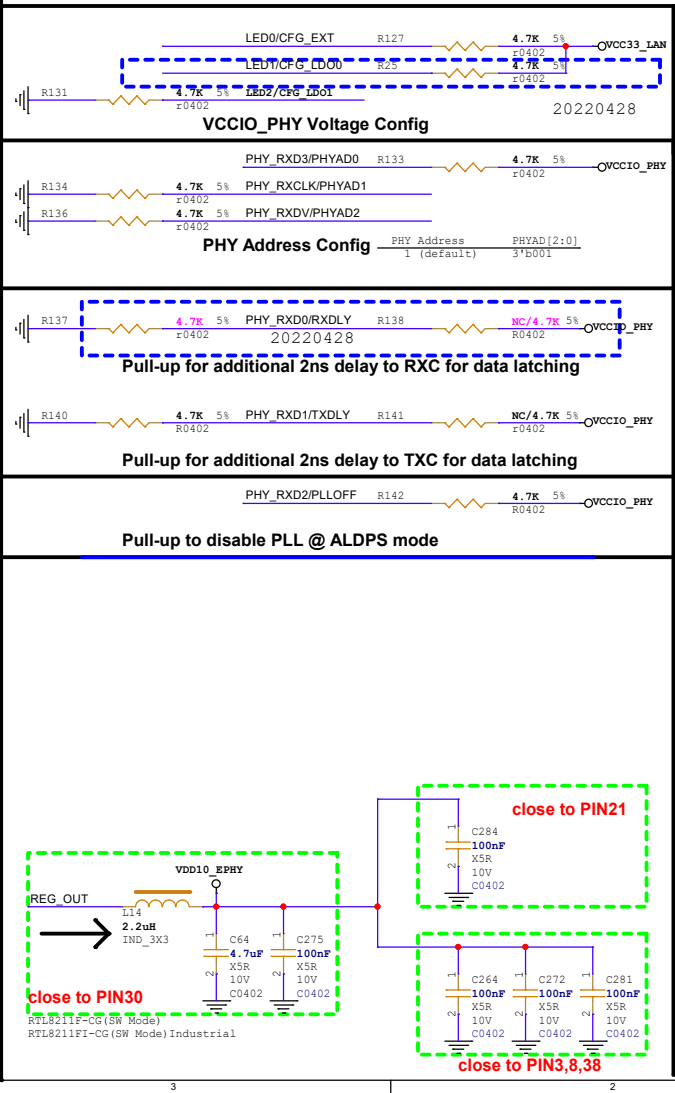
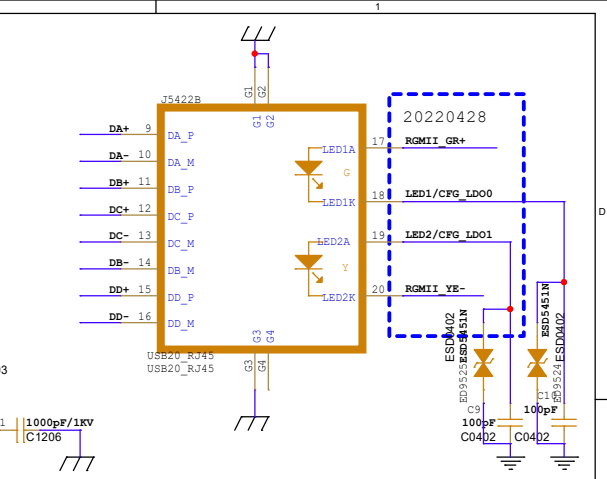
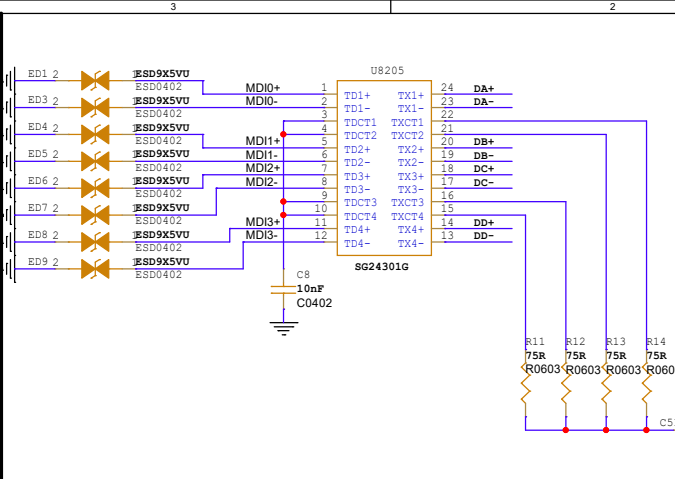
NOTES: The SATA differential trace impedance is 100 OHM.
The SATA trace length is less than 5 inch.



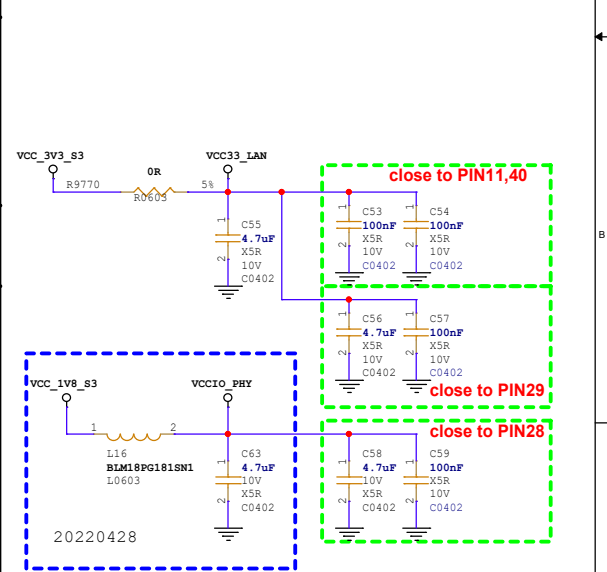
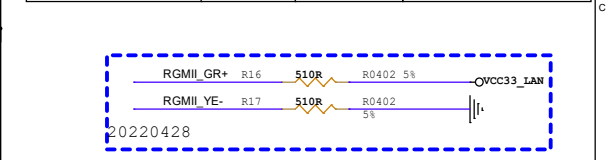
USB2.0 HUB





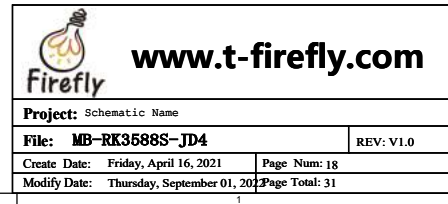
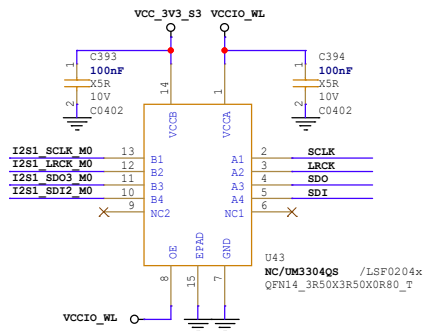
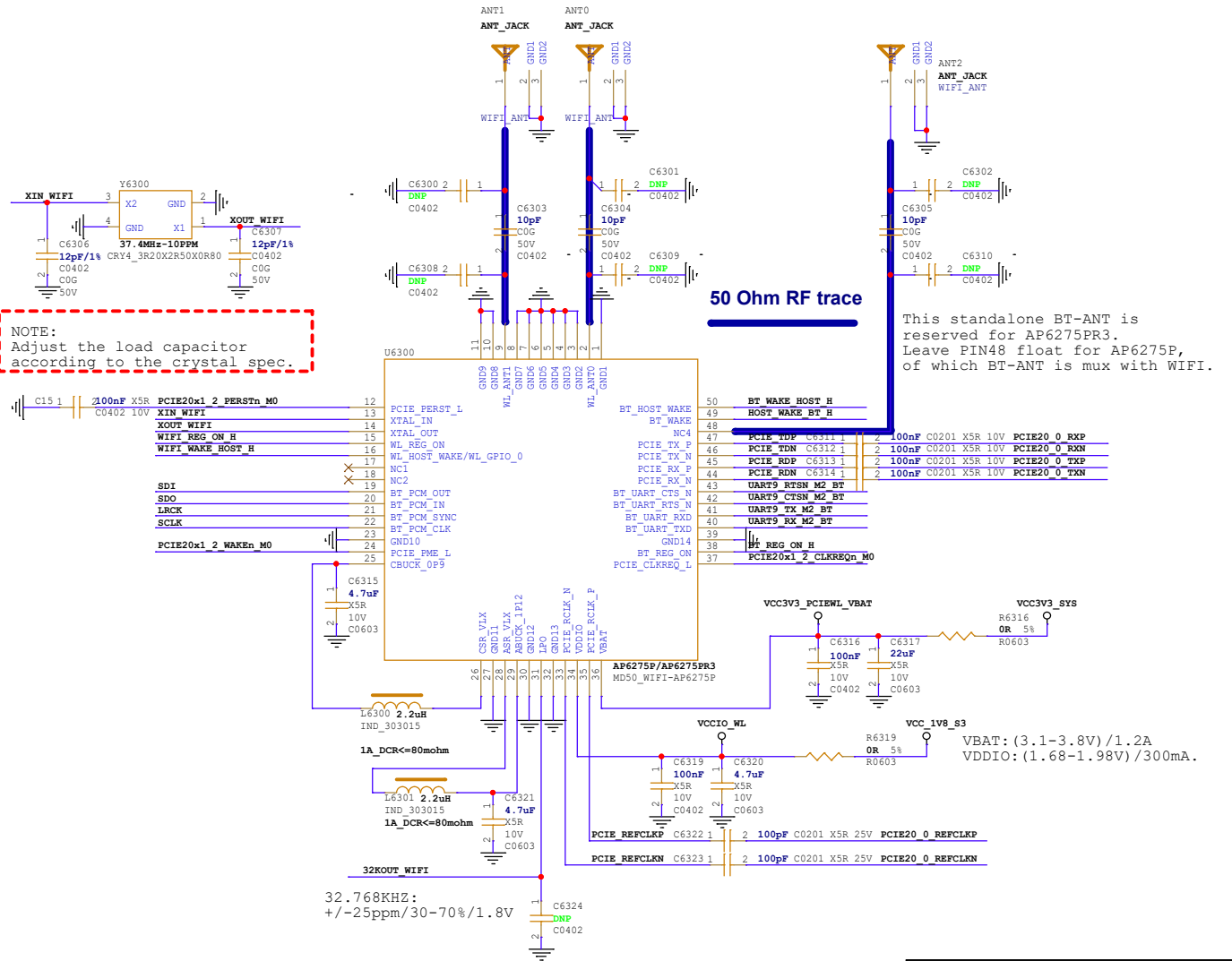
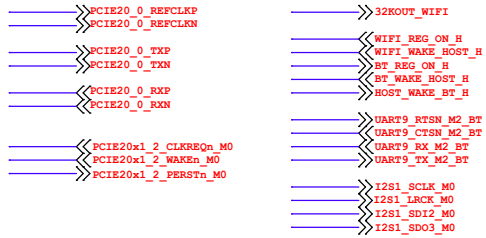


RGMI1 Power Source	CFG_EXT	CFG_LDO[1:0]	
External 3.3V	1'b1	2'b00	
External 1.8V (default)	1'b1	2'b10	
Internal 1.8V	1'b0	2'b10	

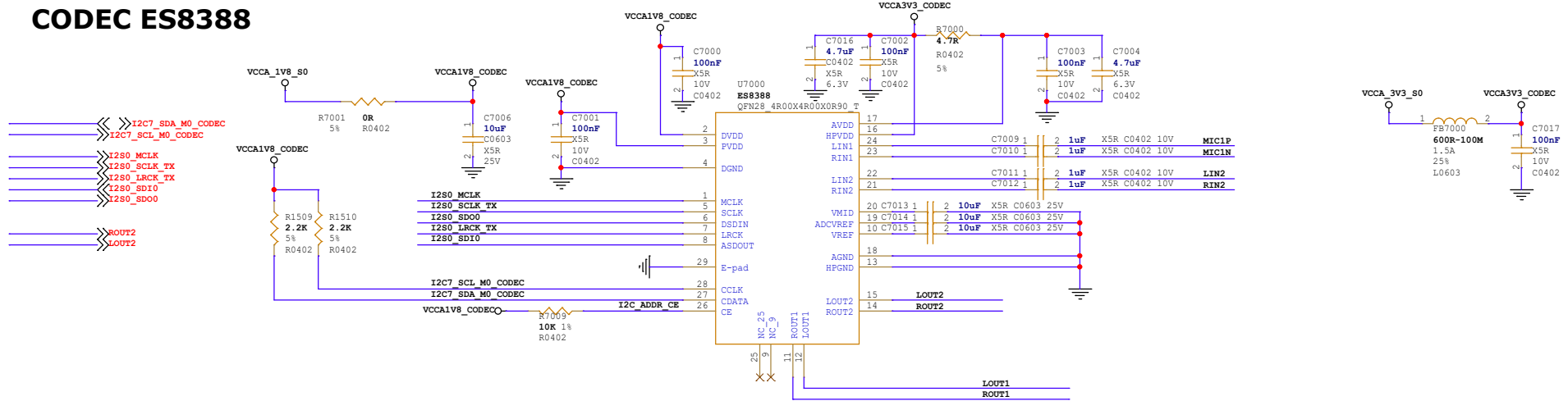


 www.t-firefly.com Firefly	
Project: Schematic Name	
File: MB-RK3588S-JD4	REV: V1.0
Create Date: Friday, December 31, 2021	Page Num: 17
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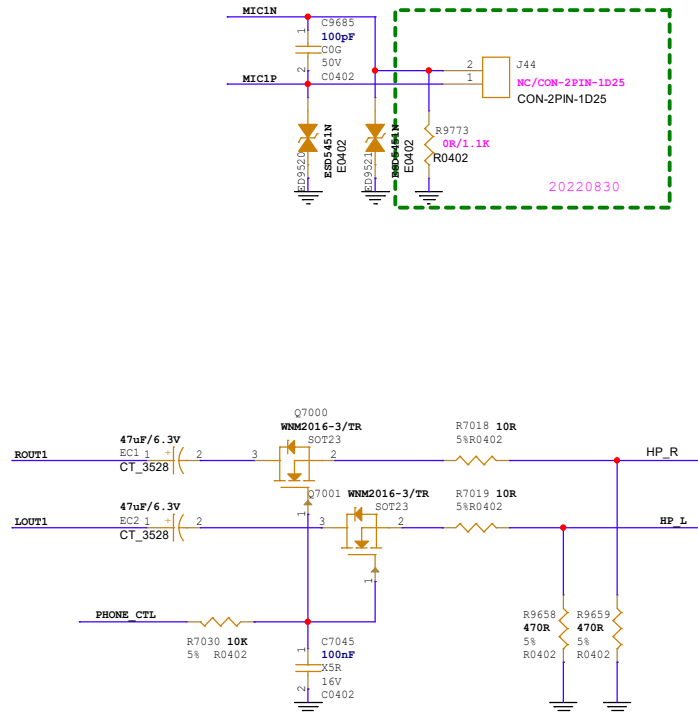
PCIe WIFI6/BT Module-2T2R



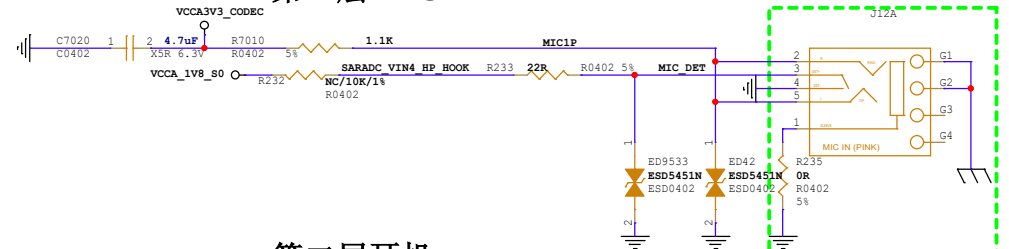
CODEC ES8388



Headphone

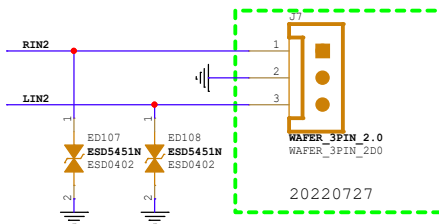
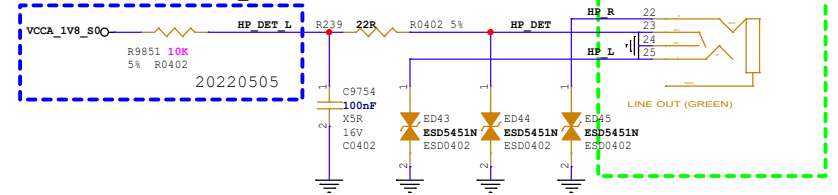


第一层MIC

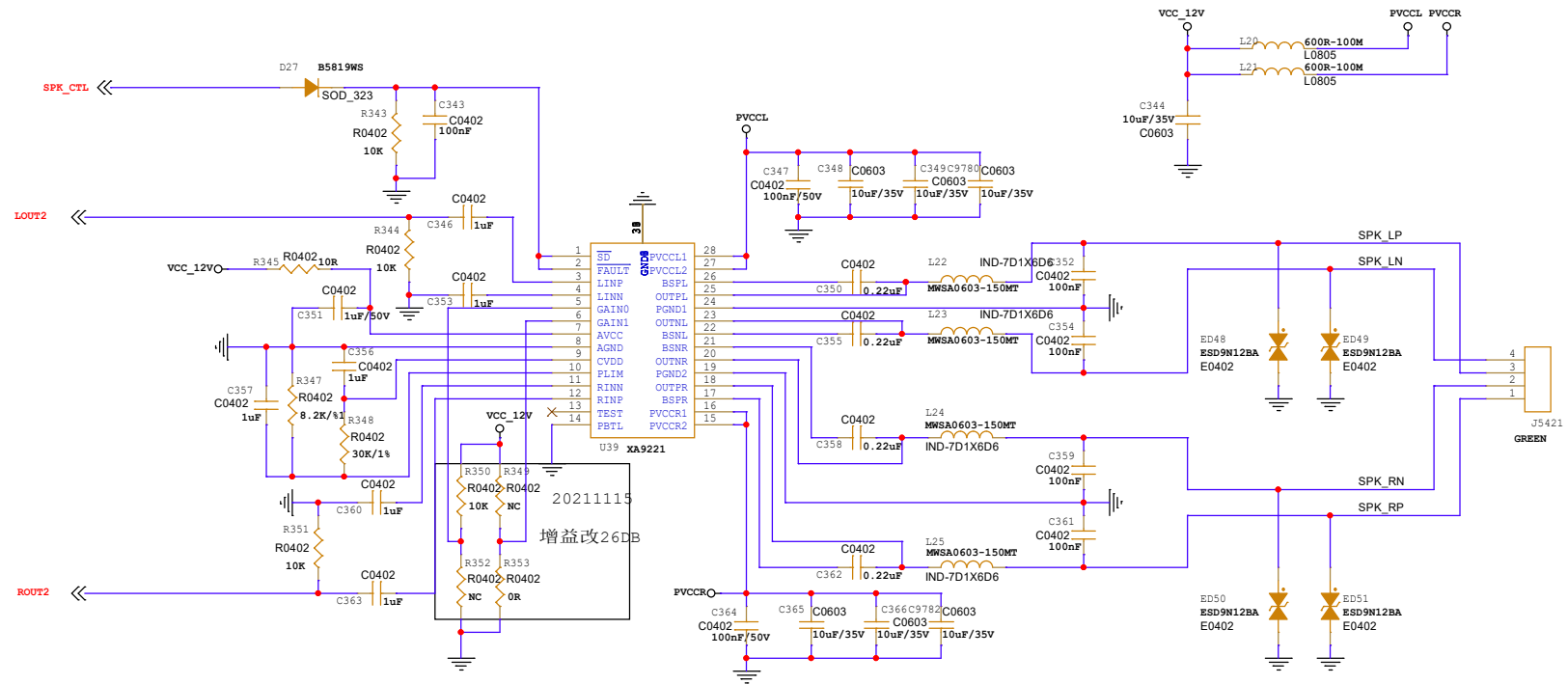


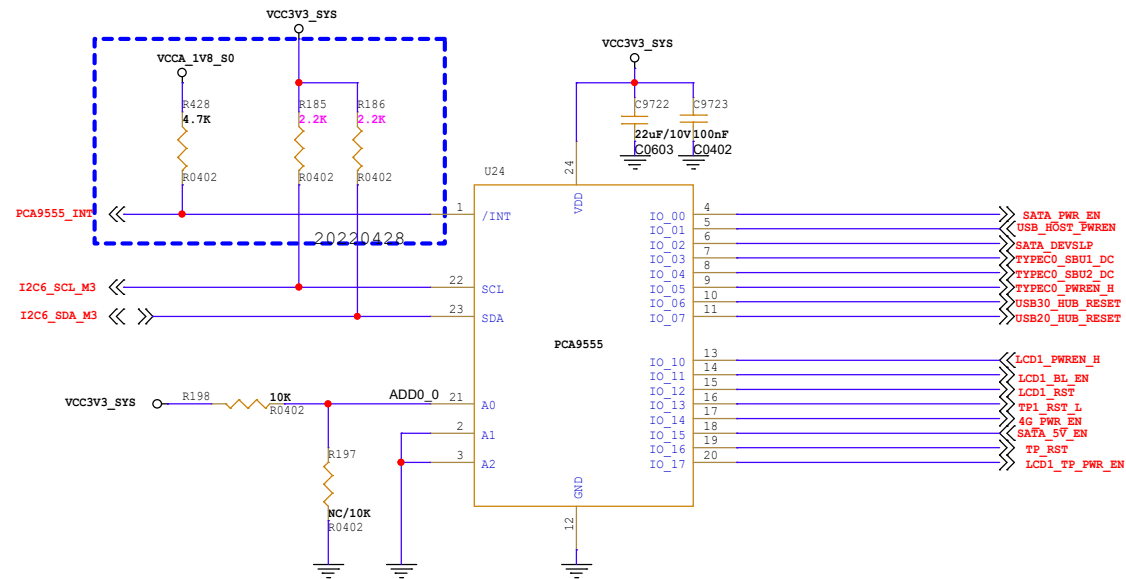
第二层耳机

有耳机插入 HP_DET 为低
无耳机插入 HP_DET 为高

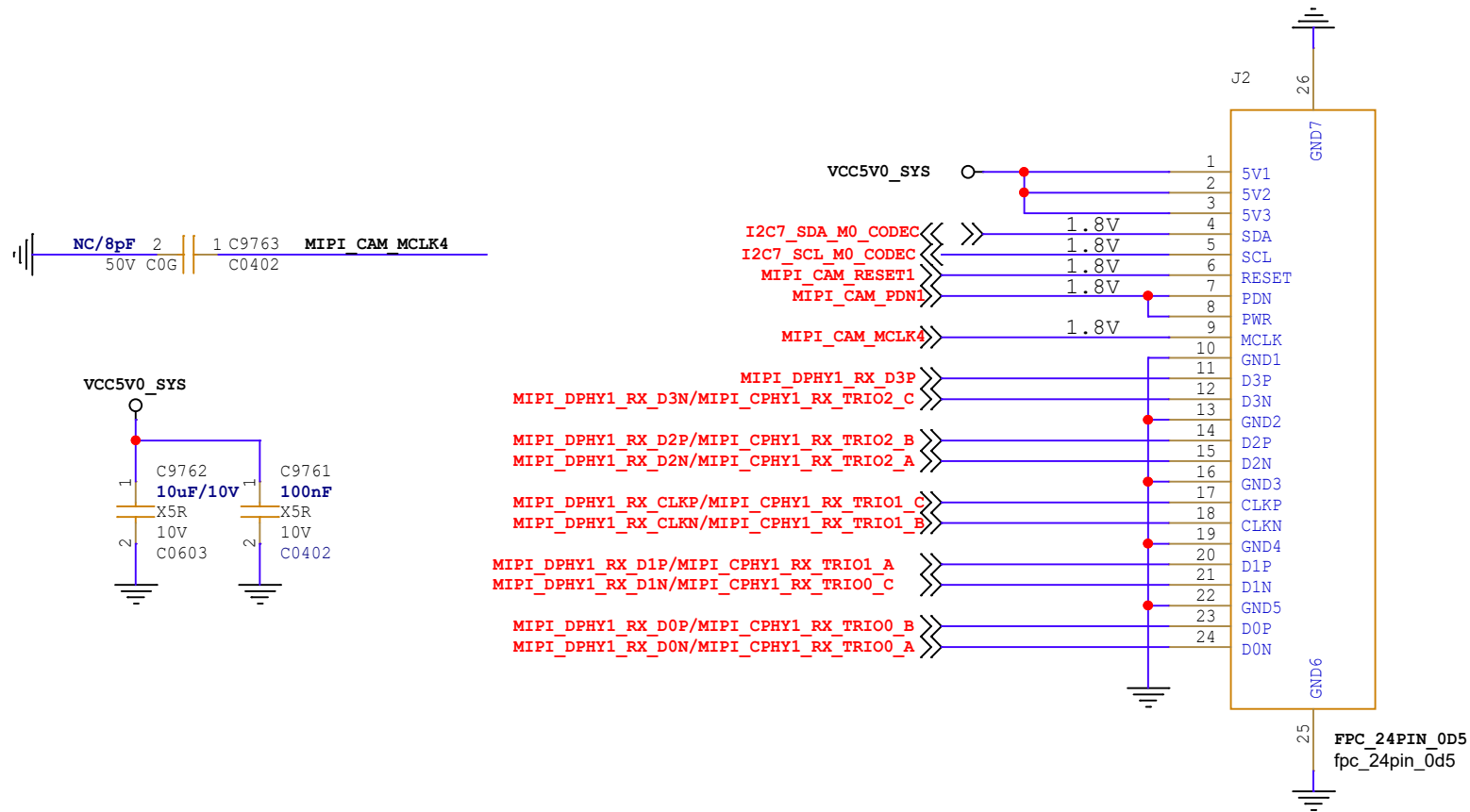


SPEAKER





MIPI-CSI_RX CON

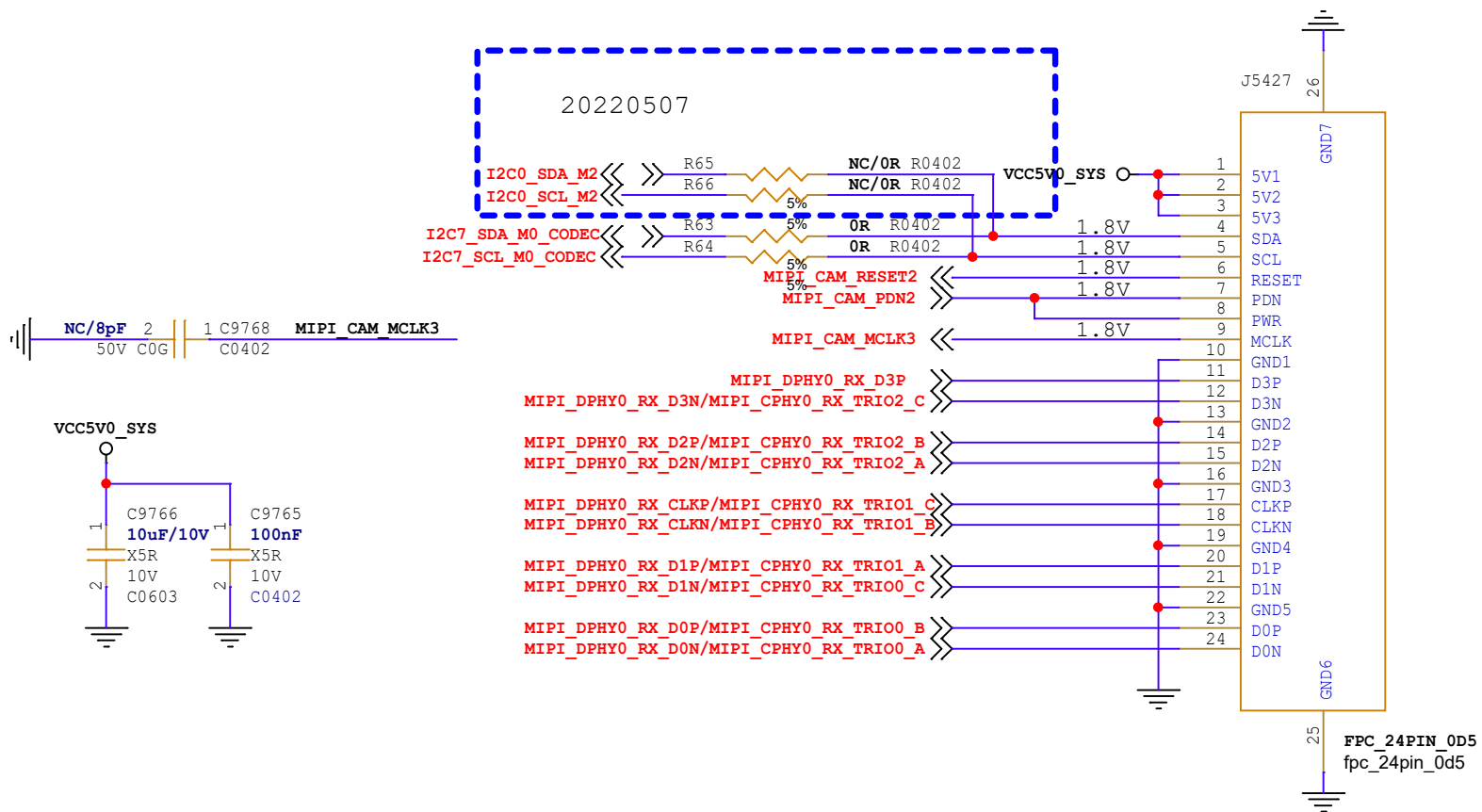




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File: MB-RK3588S-JD4	REV: V1.0
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MIPI-CSI_RX CON

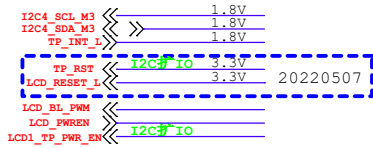
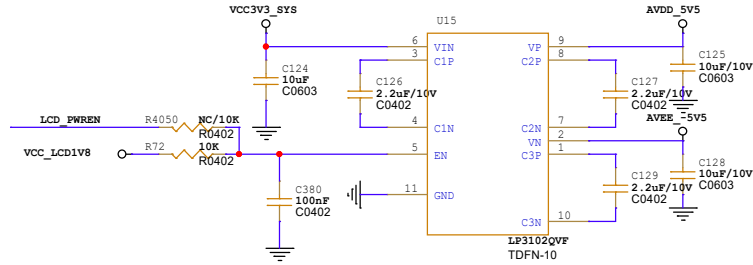



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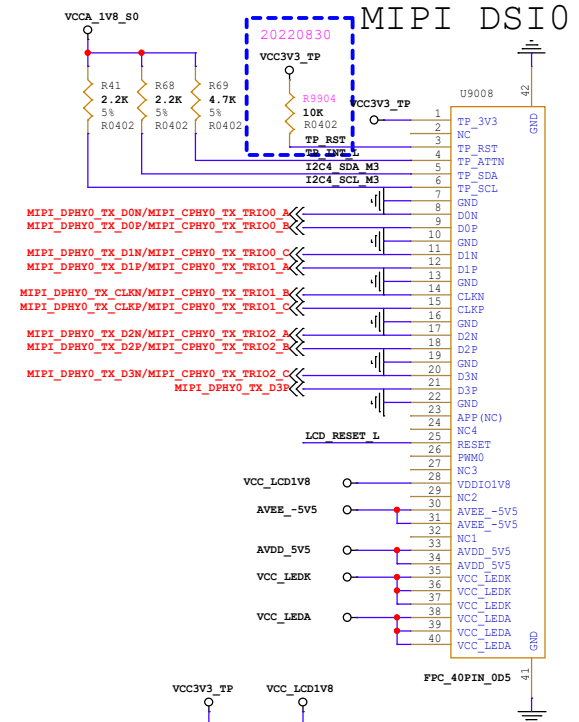
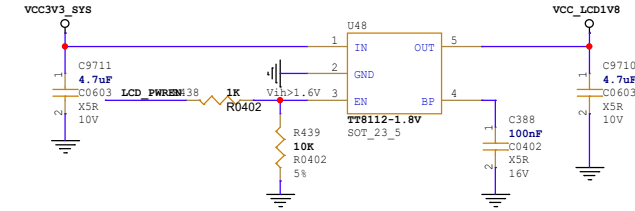
Project: Schematic Name	
File: MB-RK3588S-JD4	REV: V1.0
Create Date: Saturday, February 19, 2022	Page Num: 23
Modify Date: Saturday, September 03, 2022	Page Total: 31

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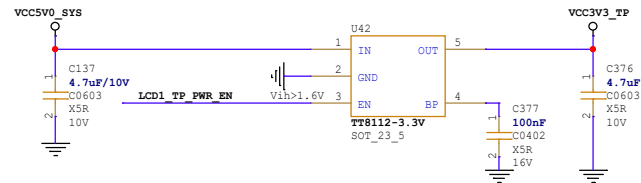
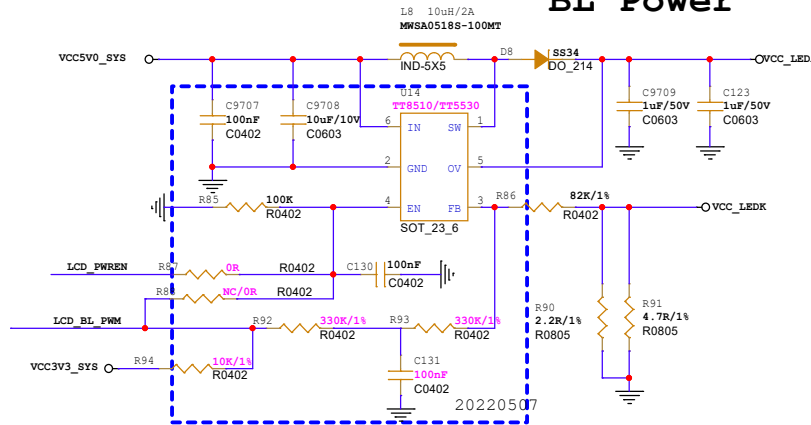
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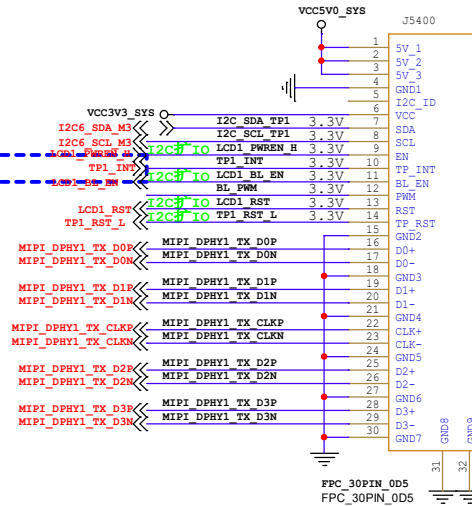
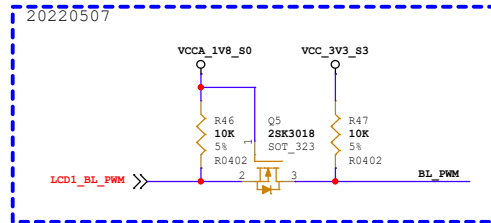
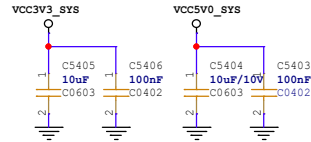
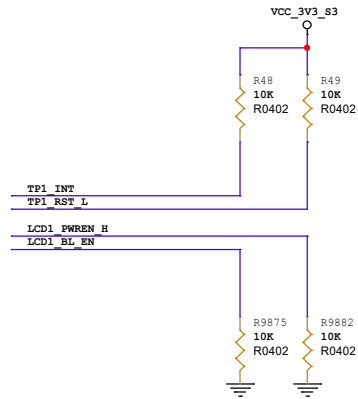


VDD



BL Power

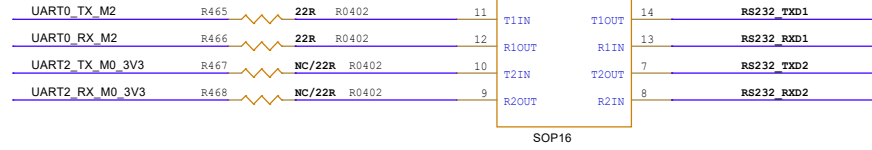


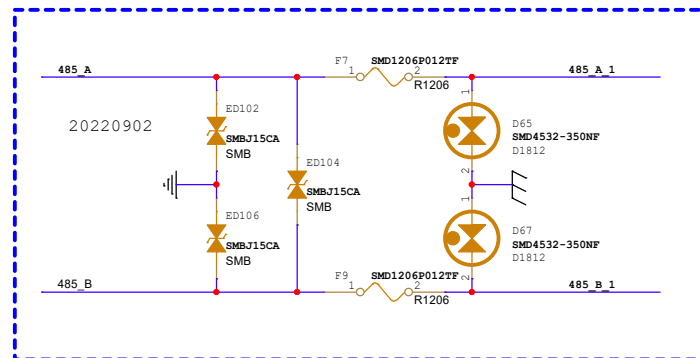
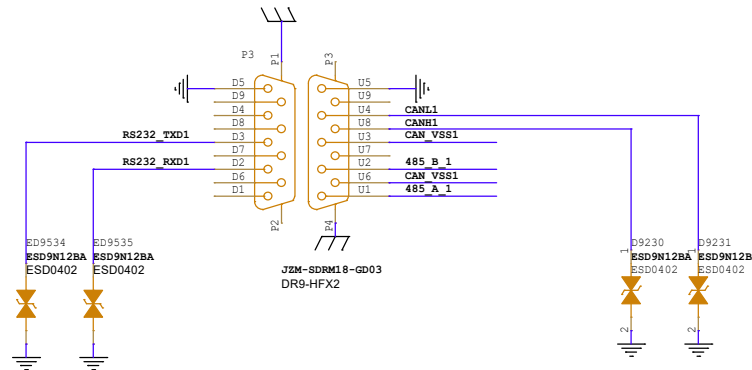
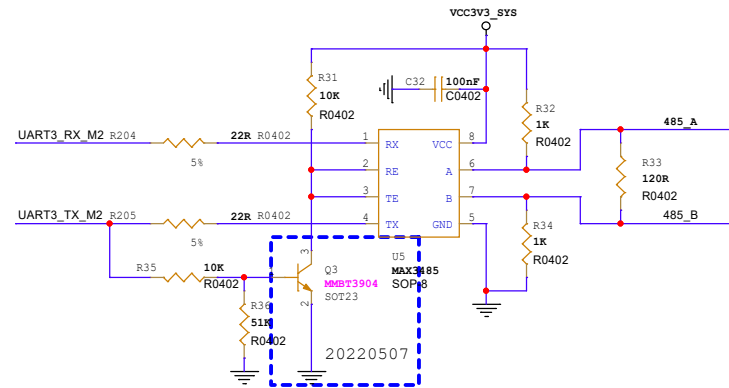
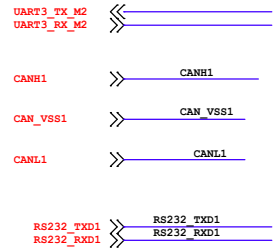


UART0_TX_M2
UART0_RX_M2
UART2_TX_M0_3V3
UART2_RX_M0_3V3

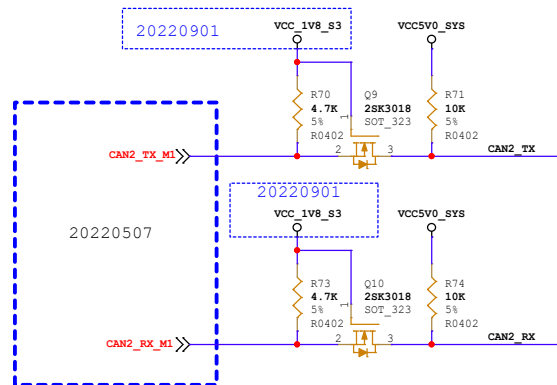
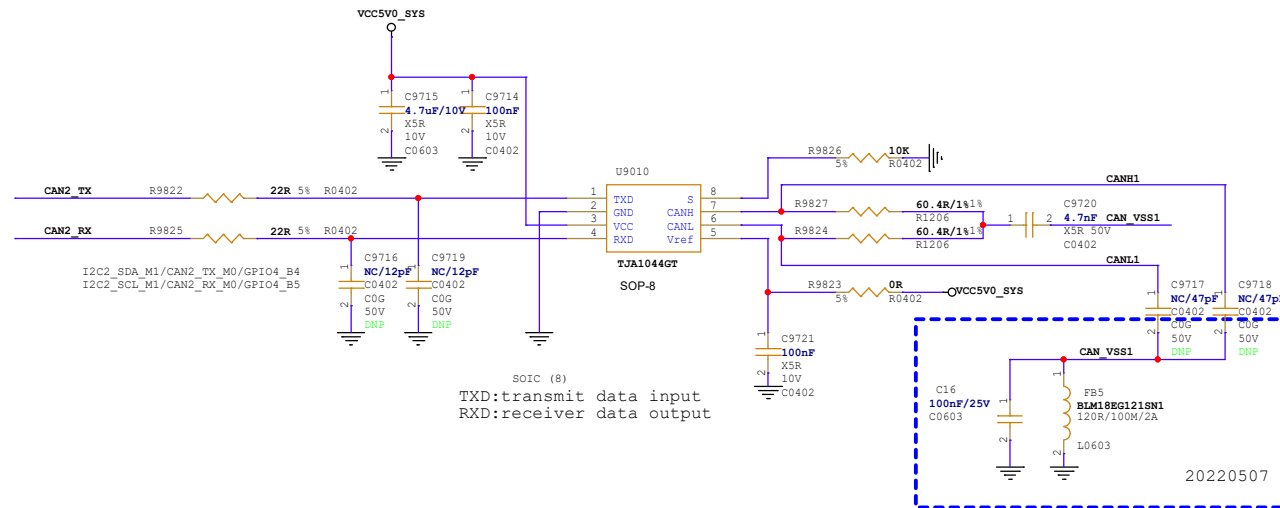
RS232_TXD1
RS232_RXD1

RS232_TXD2
RS232_RXD2



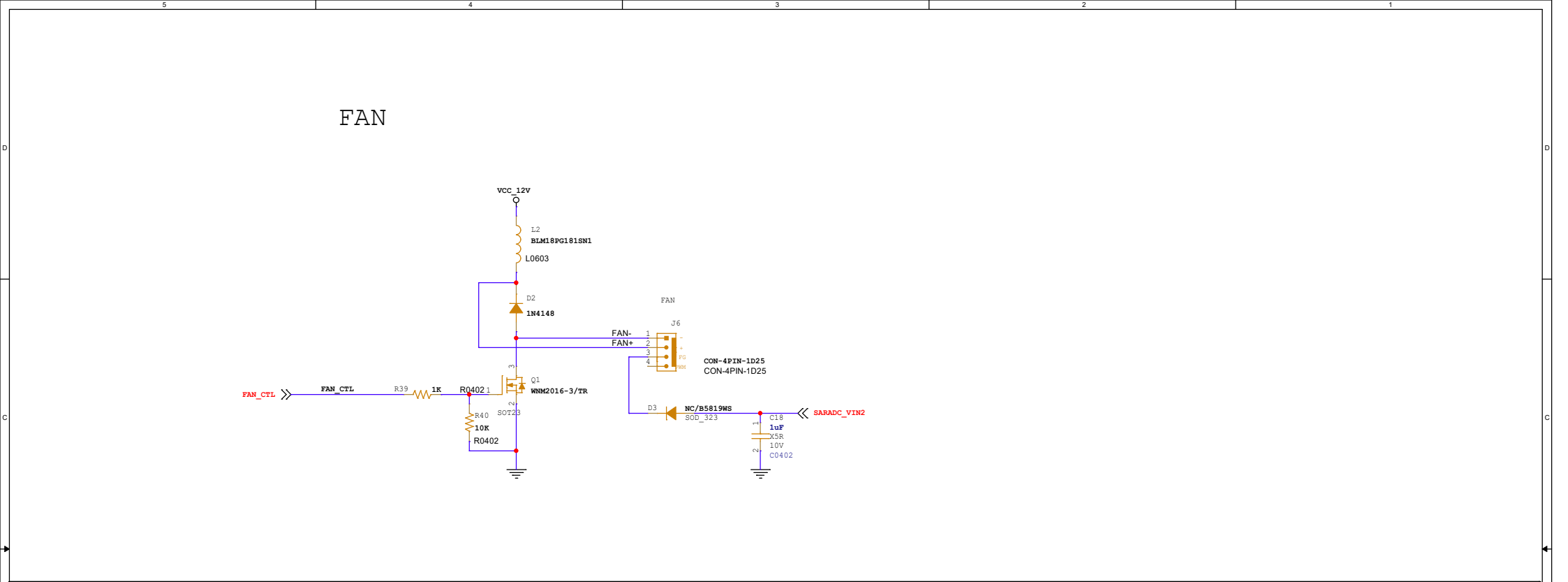


CANH1 >> CANH1
CAN_VSS1 >> CAN_VSS1
CANL1 >> CANL1



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The schematic diagram illustrates a fan control circuit. It features a 12V DC supply (VCC_12V) connected to an inductor L2 (BLM18PG181SN1, L0603). The inductor is connected to a diode D2 (1N4148). The diode's cathode is connected to the MOSFET Q1 (WNM2016-3/TR, SOT23) drain. The MOSFET's gate is controlled by FAN_CTL through a 1K resistor R39. The MOSFET's source is connected to ground through a 10K resistor R40 and a 10K resistor R0402. The MOSFET's drain is connected to the fan's FAN- pin. The fan's FAN+ pin is connected to the SARADC_VIN2 input through a diode D3 (NC/B5819WS, SOD_323). A 1uF capacitor C18 (X5R, 10V, C0402) is connected between the SARADC_VIN2 input and ground.



LED

VCC_3V3_S3

LED_D

R358 100R R0402

R8292 100R R0402

R360 1K R0402

R362 10K R0402

R8291 10K R0402

R8290 1K R0402

Q19 8805 SOT23

Q8207 8805 SOT23

DIY_LED

WORK_LED

LED

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Project: Schematic Name

File: MB-RK3588S-JD4

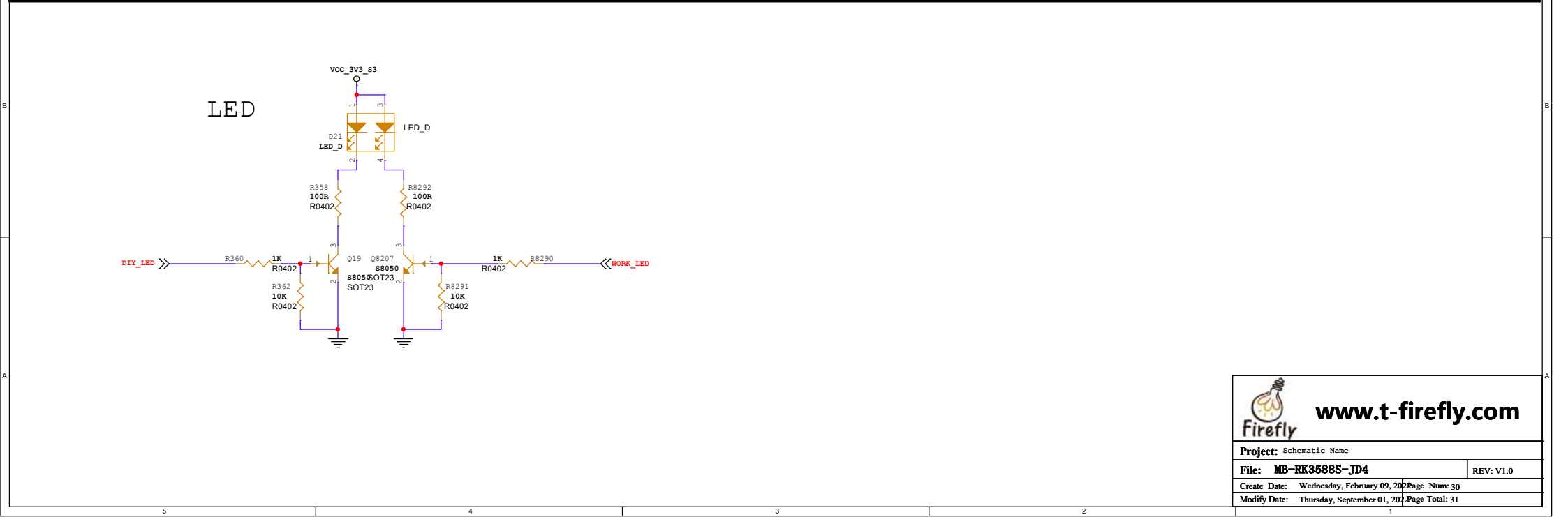
REV: V1.0

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Project: Schematic Name

Project: Schematic Name

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