

```

/dts-v1/;

#include "rk3288.dtsi"
#include <dt-bindings/input/input.h>
#include "lcd-b101ew05.dtsi"

/ {
    fiq-debugger {
        status = "disabled";
    };

```

```

@@ -1,8 +1,8 @@
/dts-v1/;

#include "rk3288.dtsi"
-#include "lcd-box.dtsi"
#include <dt-bindings/input/input.h>
+#include "lcd-b101ew05.dtsi"

/ {
    fiq-debugger {

```

```

    ts@41 {
        status = "okay";
        compatible = "firefly,gs13680";
        reg = <0x41>;
        touch-gpio = <&gpio8 GPIO_A7 IRQ_TYPE_EDGE_RISING>;
        //reset-gpio = <&gpio8 GPIO_A6 GPIO_ACTIVE_LOW>;
        max-x = <1280>;
        max-y = <800>;
        flip-x = <0>;
        flip-y = <0>;
        swap-xy = <1>;
    };
    mpu6050:mpu6050{

```

```

@@ -511,7 +511,7 @@
    };

    ts@41 {
-        status = "disabled";
+        status = "okay";
        compatible = "firefly,gs13680";
        reg = <0x41>;
        touch-gpio = <&gpio8 GPIO_A7 IRQ_TYPE_EDGE_RISING>;

```

```

    rk1000_tve@42 {
        compatible = "rockchip,rk1000_tve";
        reg = <0x42>;
        gpio-switch = <&gpio3 GPIO_D4 GPIO_ACTIVE_LOW>;
        rockchip,source = <0>; //0: LCDC0; 1: LCDC1
        rockchip,prop = <PRMR>;//<EXTEND>
    };
    rk1000_codec: rk1000_codec@60 {

```

```

@@ -576,7 +576,7 @@
    gpio-pwn = <&gpio0 GPIO_C1 GPIO_ACTIVE_HIGH>;
    rockchip,source = <0>; //0: LCDC0; 1: LCDC1
    rockchip,prop = <PRMR>;//<EXTEND>
-    status = "okay";
+    status = "disabled";
    };
};

```

```

/*lcde0 as PRMR(HDMI),lcde1 as EXTEND(RK1000)*/
alcde0 {
    status = "okay";
    rockchip,iommu-enabled = <1>;
    rockchip,prop = <PRMR>;
    power_ctr: power_ctr {
        rockchip,debug = <1>;
        lcd_en:lcd_en {
            rockchip,power_type = <GPIO>;
            gpios = <&gpio7 GPIO_A3 GPIO_ACTIVE_HIGH>;
            rockchip,delay = <18>;
        };

        lcd_cs:lcd_cs {
            rockchip,power_type = <GPIO>;
            gpios = <&gpio7 GPIO_A4 GPIO_ACTIVE_HIGH>;
            rockchip,delay = <10>;
        };

        lcd_rst:lcd_rst {
            rockchip,power_type = <GPIO>;
            gpios = <&gpio0 GPIO_B5 GPIO_ACTIVE_LOW>;
            rockchip,delay = <10>;
        };
    };
};

```

```

@@ -611,6 +611,28 @@
    status = "okay";
    rockchip,iommu-enabled = <1>;
    rockchip,prop = <PRMR>;
+
+    power_ctr: power_ctr {
+        rockchip,debug = <1>;
+        lcd_en:lcd_en {
+            rockchip,power_type = <GPIO>;
+            gpios = <&gpio7 GPIO_A3 GPIO_ACTIVE_HIGH>;
+            rockchip,delay = <10>;
+        };
+
+        lcd_cs:lcd_cs {
+            rockchip,power_type = <GPIO>;
+            gpios = <&gpio7 GPIO_A4 GPIO_ACTIVE_HIGH>;
+            rockchip,delay = <10>;
+        };
+
+        lcd_rst:lcd_rst {
+            rockchip,power_type = <GPIO>;
+            gpios = <&gpio0 GPIO_B5 GPIO_ACTIVE_LOW>;
+            rockchip,delay = <10>;
+        };
+
+    };
+
+};
+

```

```

&lcde1 {
    status = "okay";
    rockchip,iommu-enabled = <1>;
    rockchip,prop = <EXTEND>;
};

```

```

@@ -690,7 +712,7 @@
*/

&pwm1 {
-    status = "disabled";
+    status = "okay";
};

--

```